



\$AF  
2811

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yamazaki, et al. Art Unit : 2811  
 Serial No.: 09/814,255 Examiner : Ori Nadav  
 Filed : March 21, 2001  
 Title : SEMICONDUCTOR DEVICE AND ITS MANUFACTURING METHOD

#9  
1-15-03  
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Commissioner for Patents  
 Washington, D.C. 20231

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed.  
 The required fees are computed below. Please apply any charges  
 not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	19	-	27	=	0	\$0
Independent	7	-	7	=	0	\$0
First Presentation of Multiple Dependent Claims						\$0

Applicant hereby petitions under 37 C.F.R. §1.136  
 for a 1 month extension of time. \$110

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TOTAL FEE DUE \$110

A check for \$110 is attached.

Respectfully submitted,

Date: 1-3-2003

Dwight U. Thompson  
 Dwight U. Thompson  
 Reg. No. P-53,688

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this  
 correspondence is being deposited with the  
 United States Postal Service as first class mail  
 with sufficient postage on the date indicated  
 below and is addressed to the Commissioner for  
 Patents, Washington, D.C. 20231.

Date of Deposit January 3, 2003  
 Signature Tobi Terry  
 Tobi Terry  
 Typed or Printed Name of Person Signing  
 Certificat

01/10/2003 CCHAU1 00000082 09814255

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